

FIG.1 A

FIG.1 B

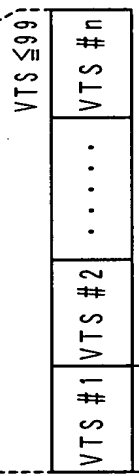


FIG.1 C

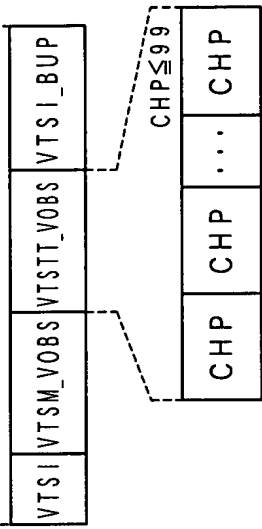
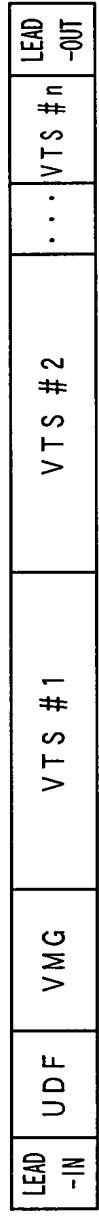
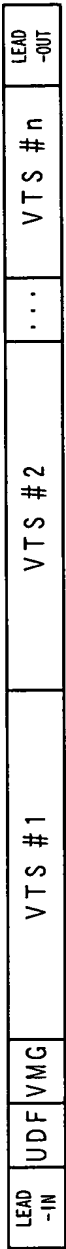
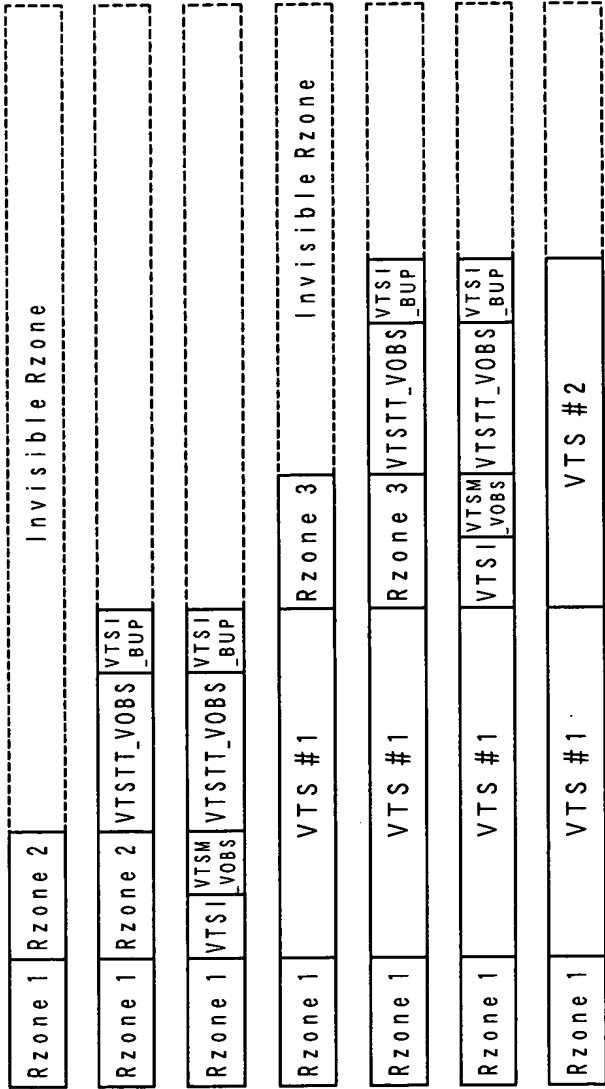


FIG.1 D







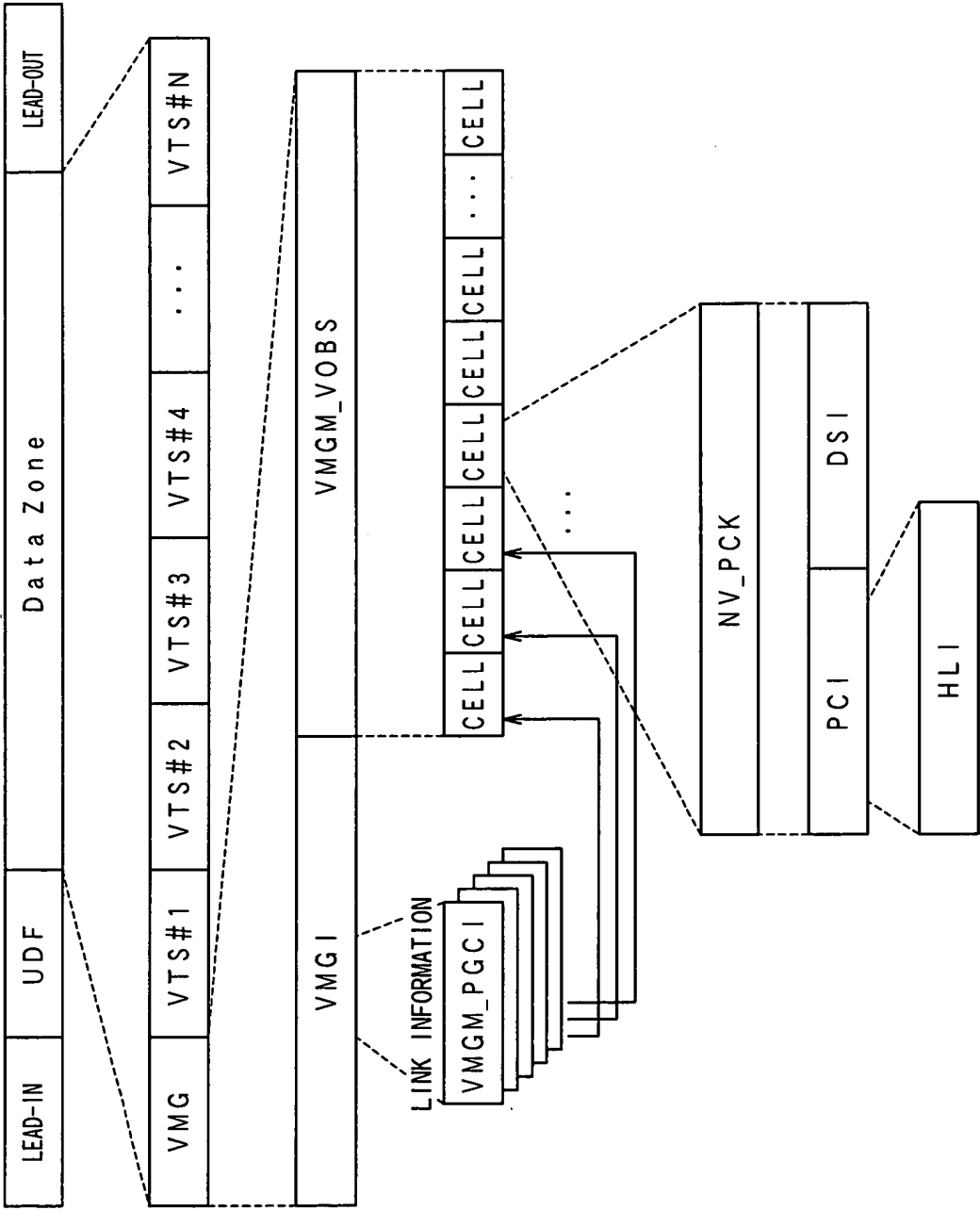


FIG.4

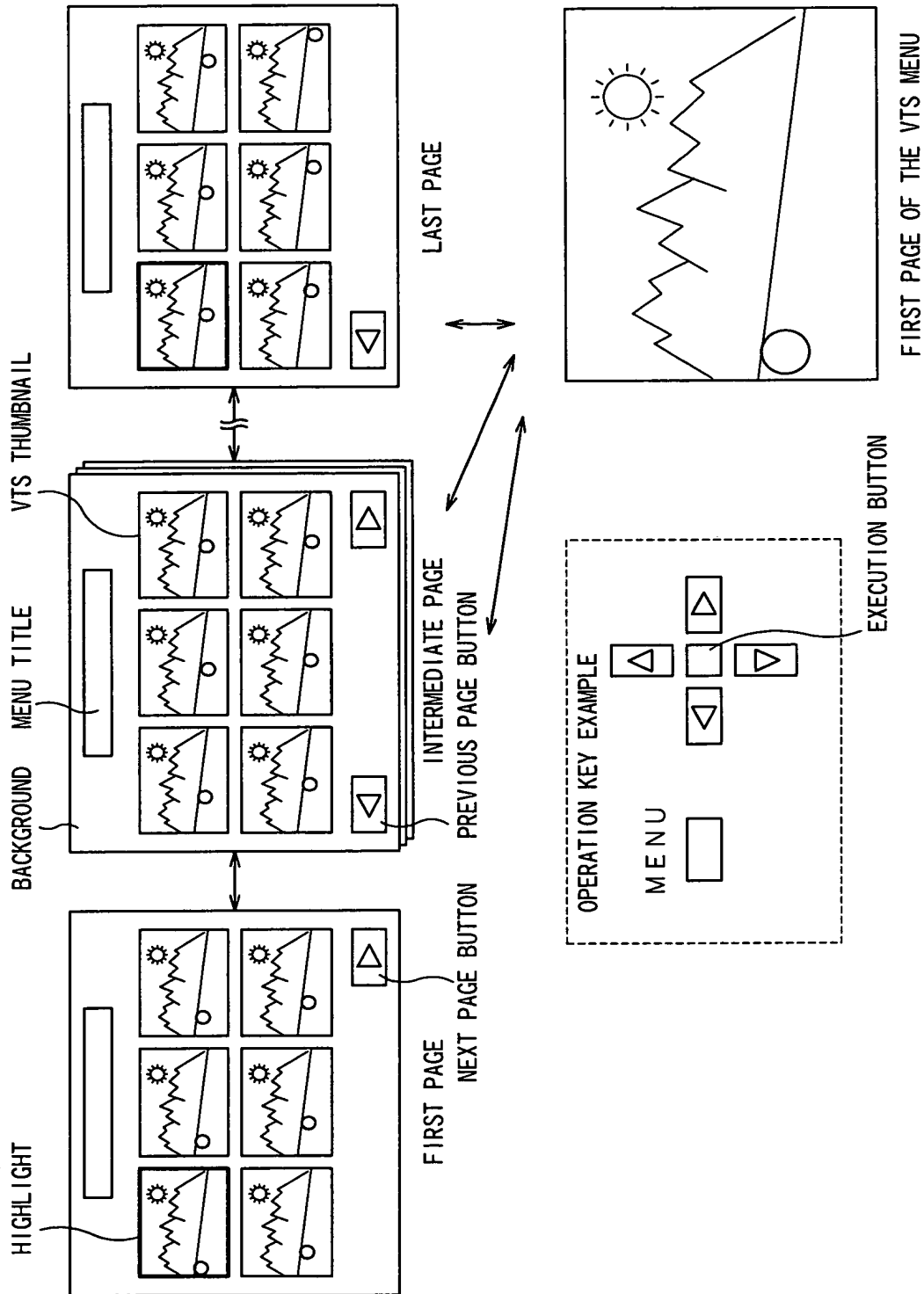


FIG.5

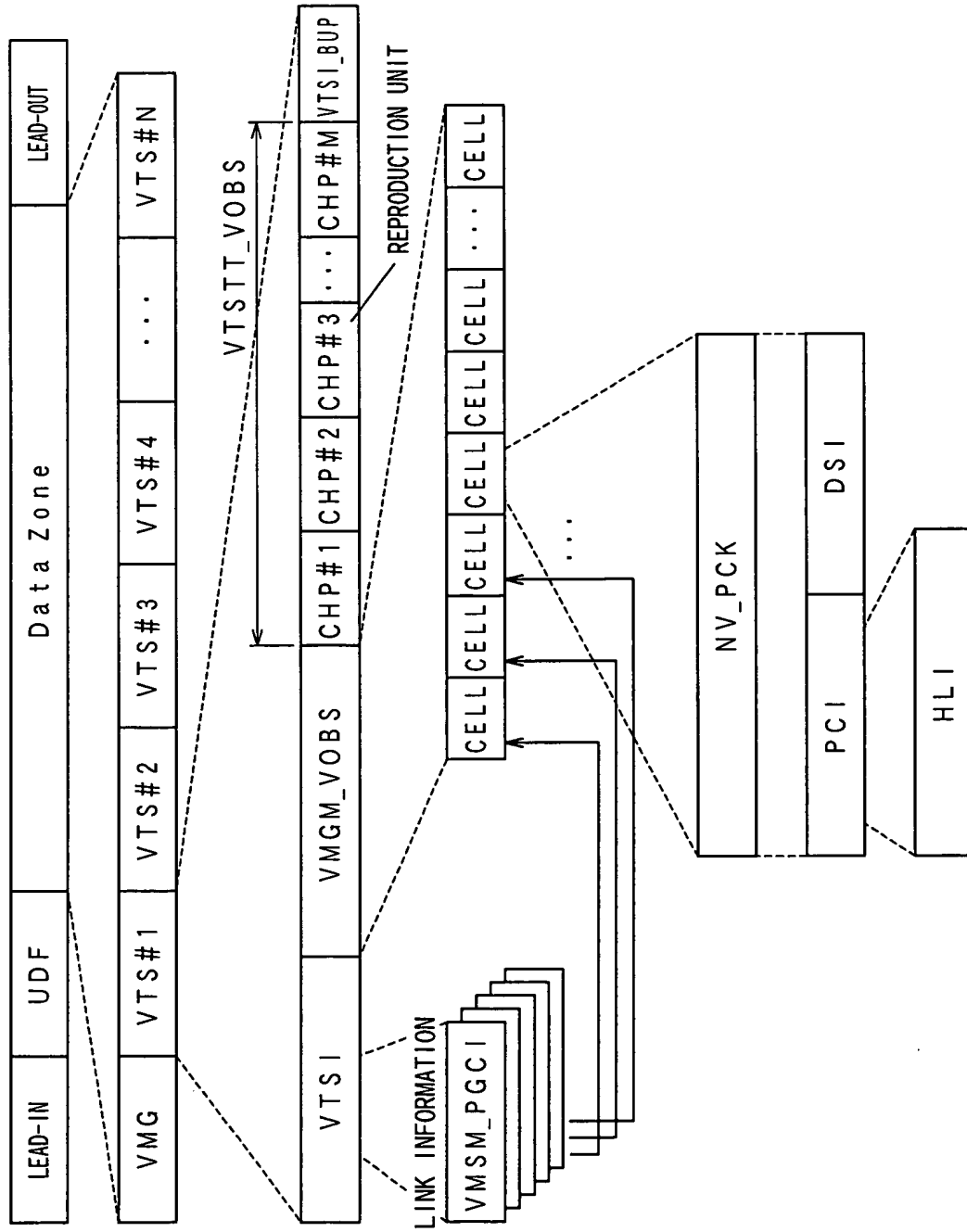


FIG.

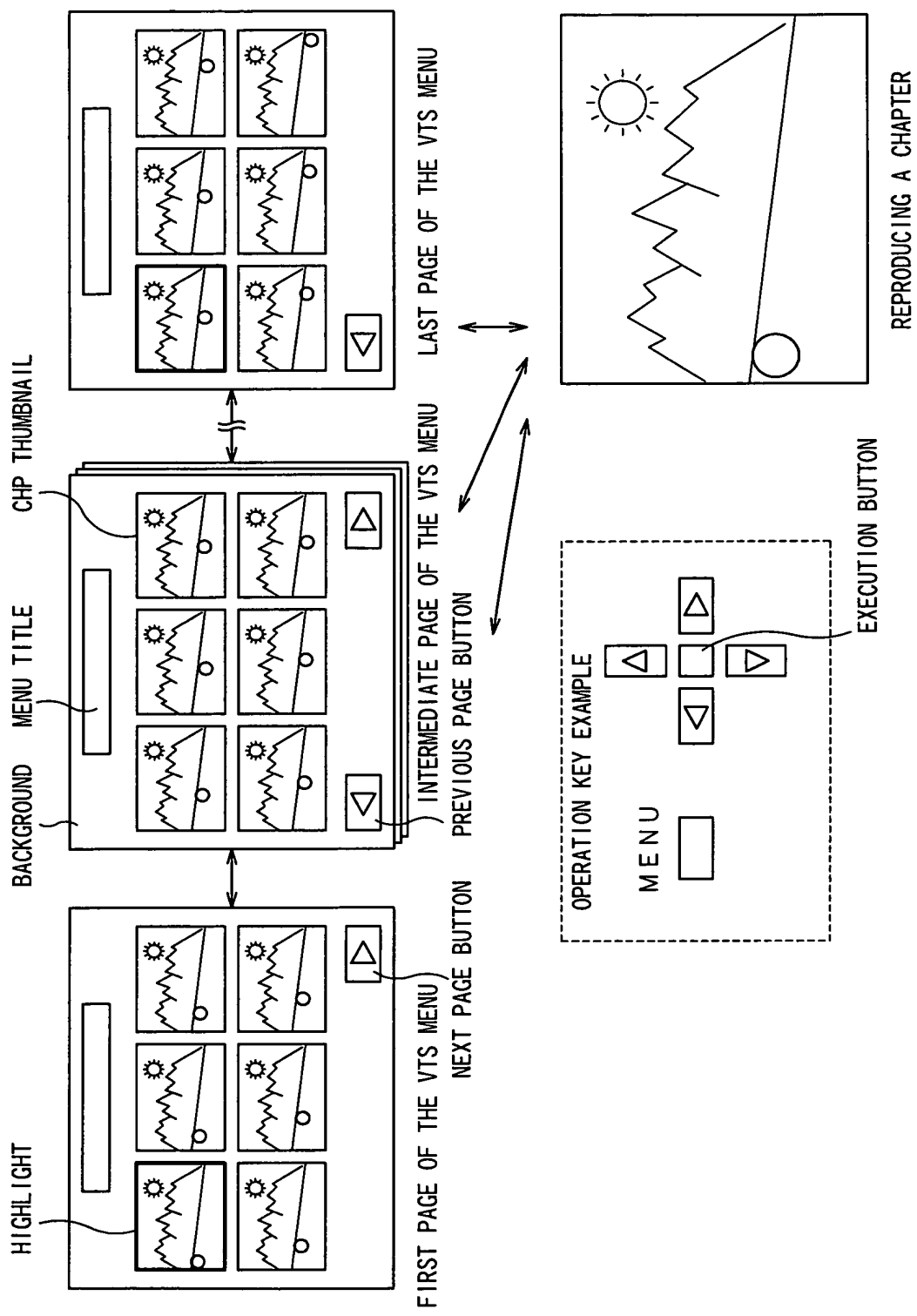


FIG.7

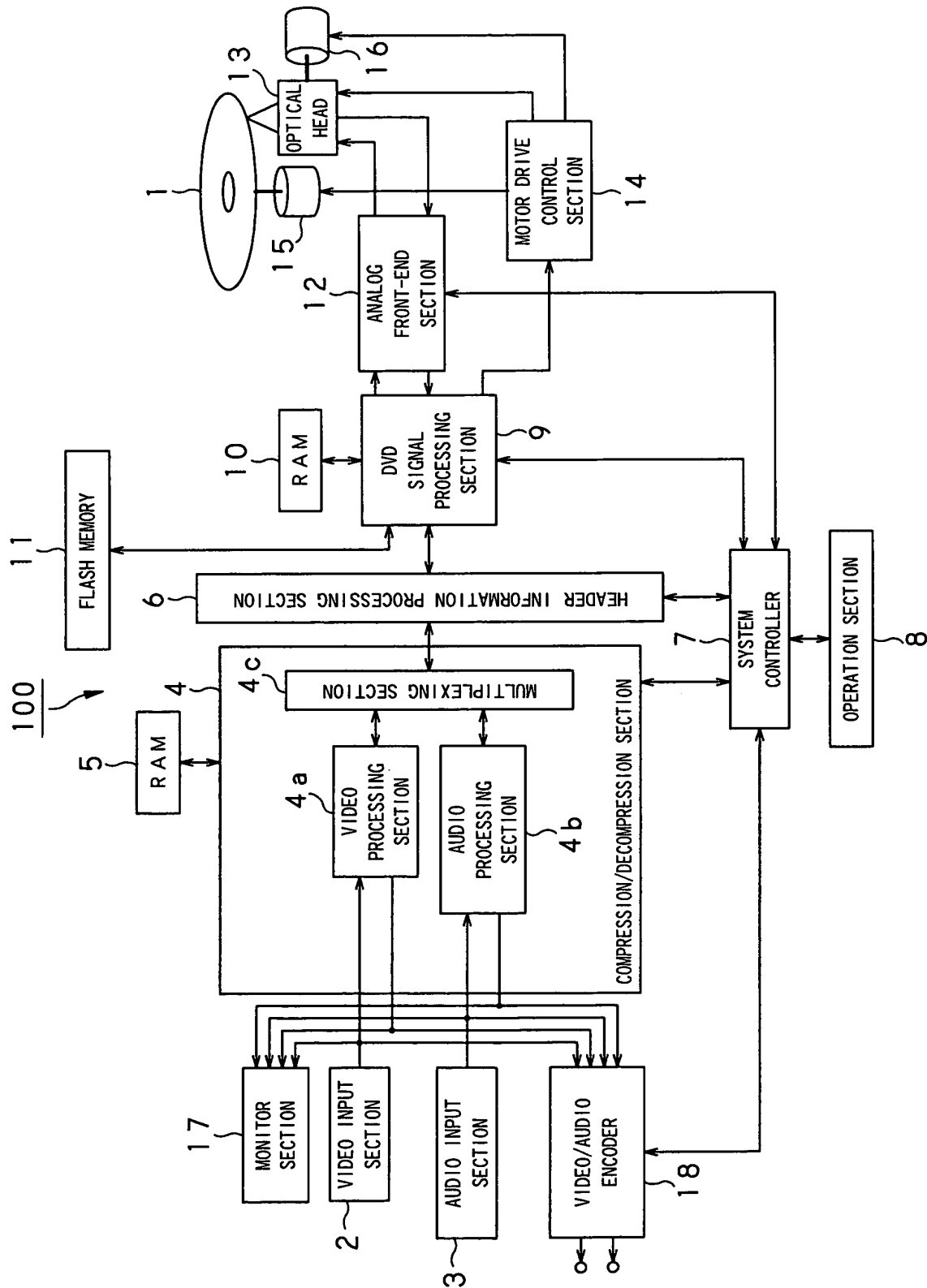


FIG. 8



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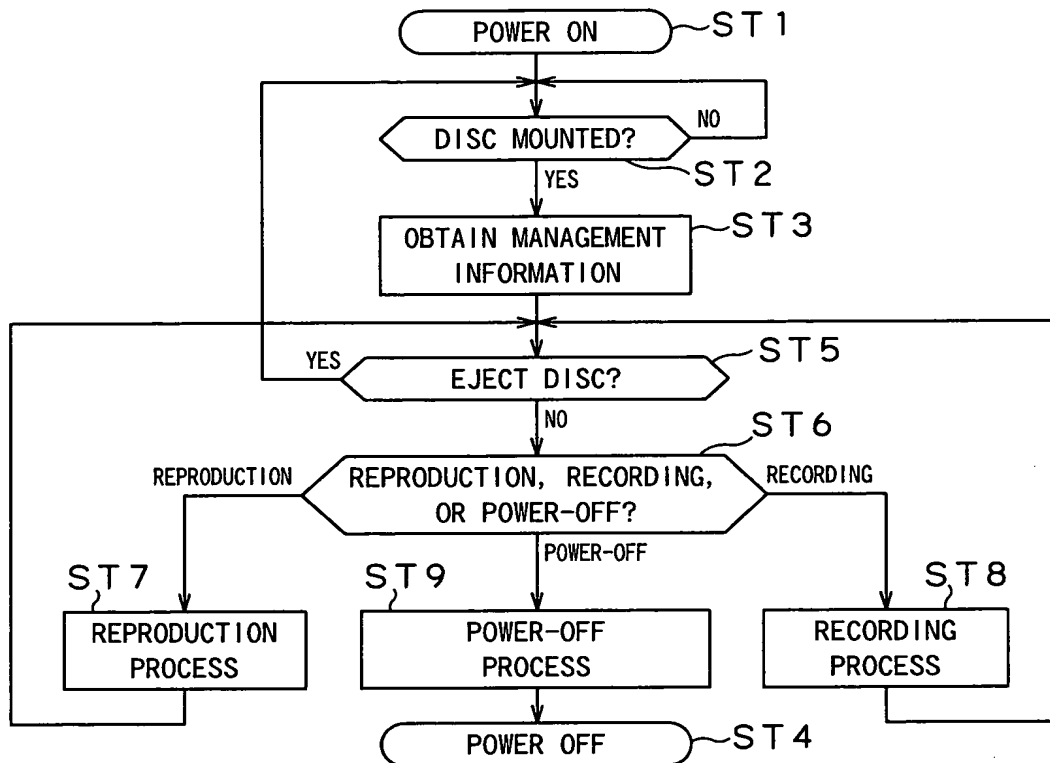


FIG. 9

Rzone 1 Rzone 2 Invisible Rzone

Rzone 1	Rzone 2	VTSTT_VOBS	VTSL_BUP

R zone 1	VTSL	TMP_VMGI	VTSTT_VOBS	VTSL_BUP

Rzone 1	VTS #1	Rzone 3	Invisible Rzone
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Rzone 1	VTS # 1	Rzone 3	VTSTT_VOBS	VTSTT BUP
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Rzone 1	VTS # 1	VTSI	TMP- VMGI	VTSTT_VOBS	VTSI BUP
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R zone 1	VTS # 1	VTS # 2
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LEAD	UDF	VTS #1	VTS #2	...	VTS #n	LEAD -OUT
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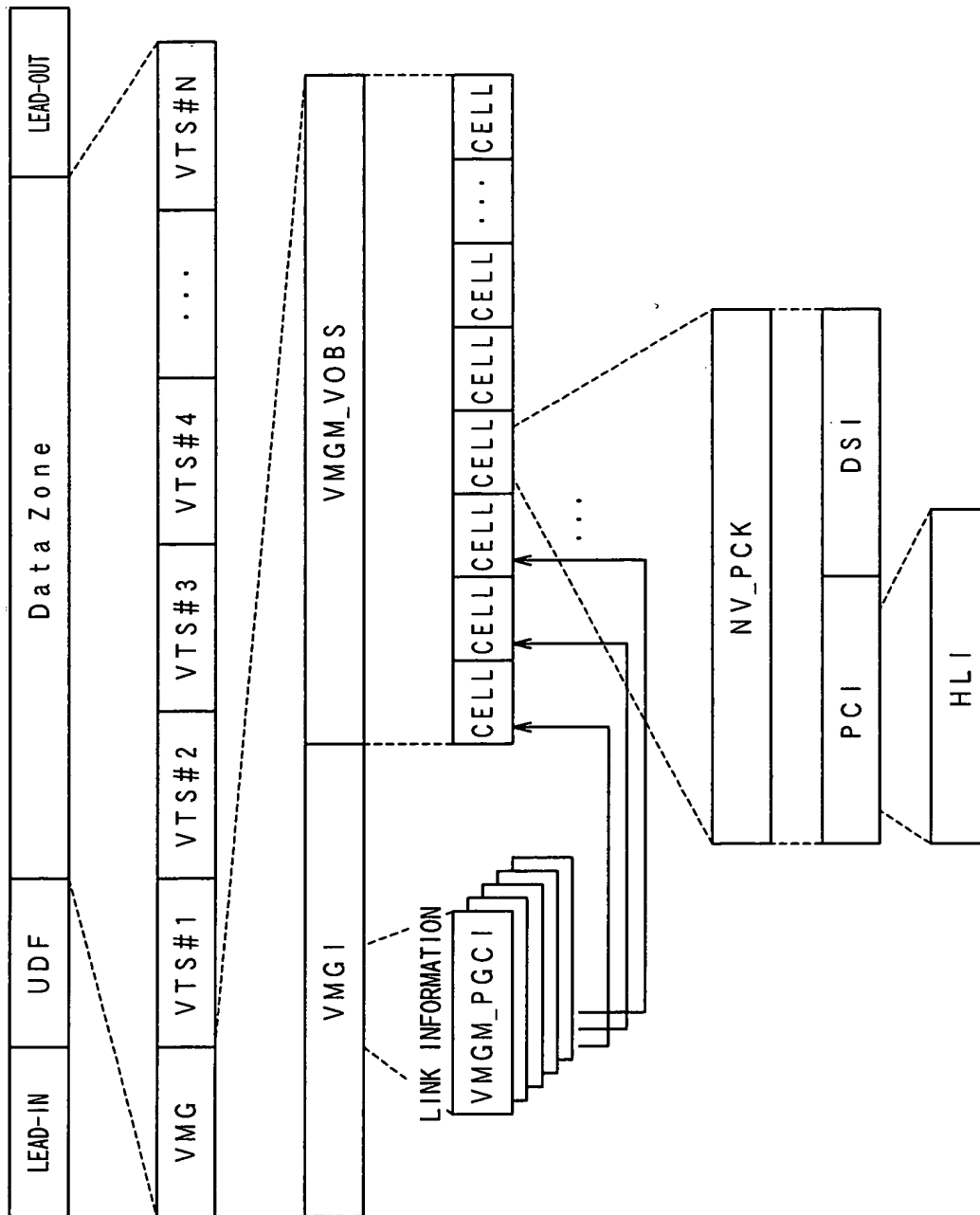


FIG. 11

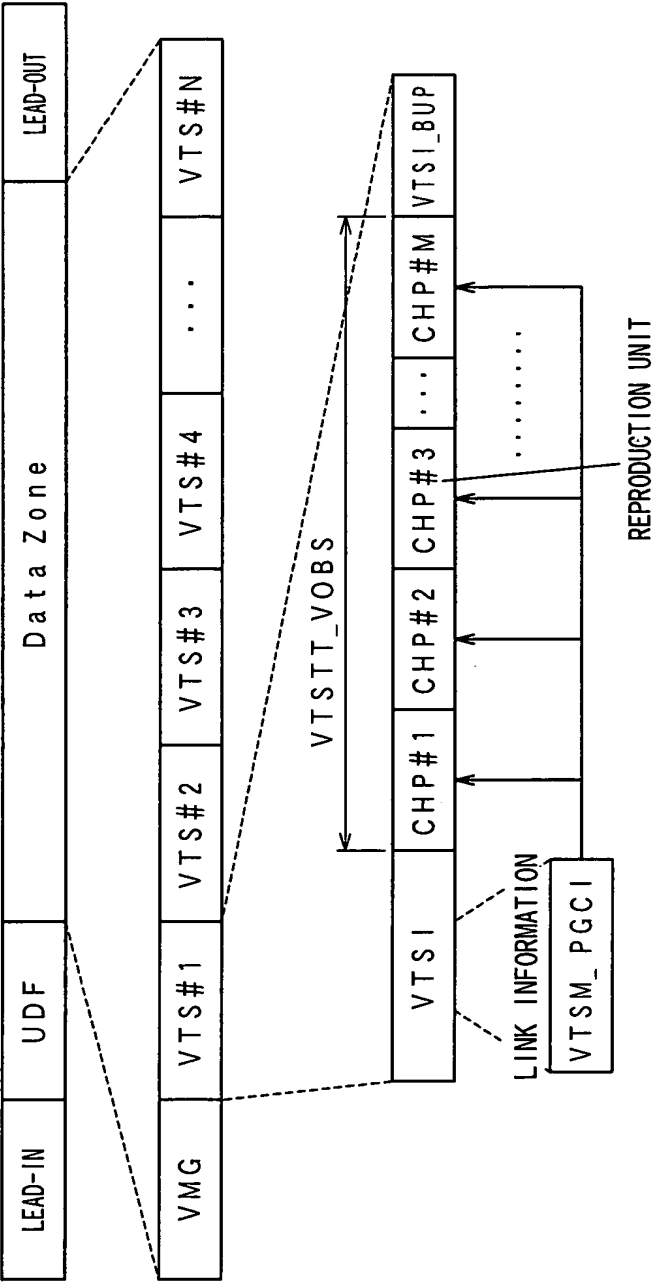


FIG.12

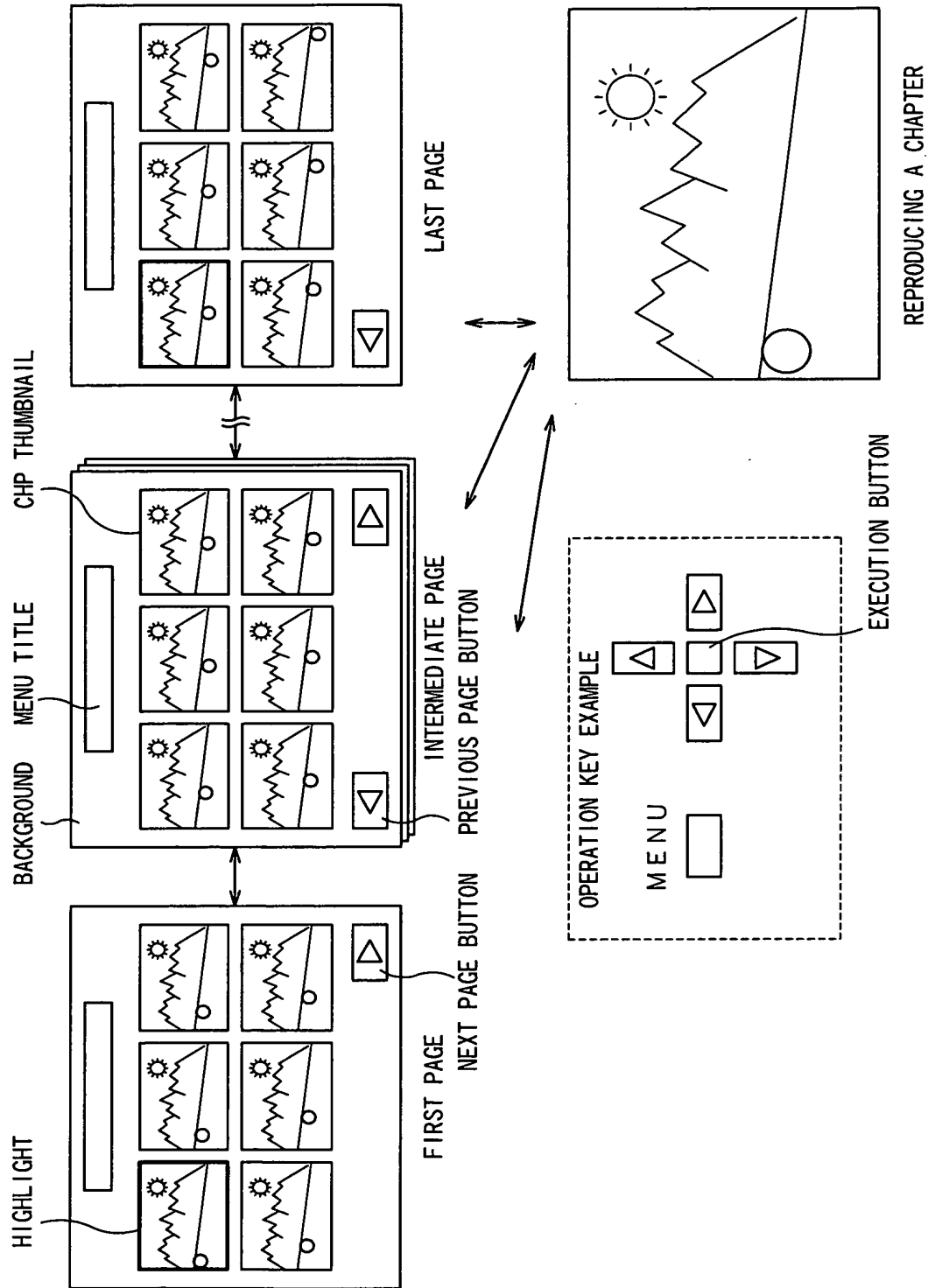


FIG.13

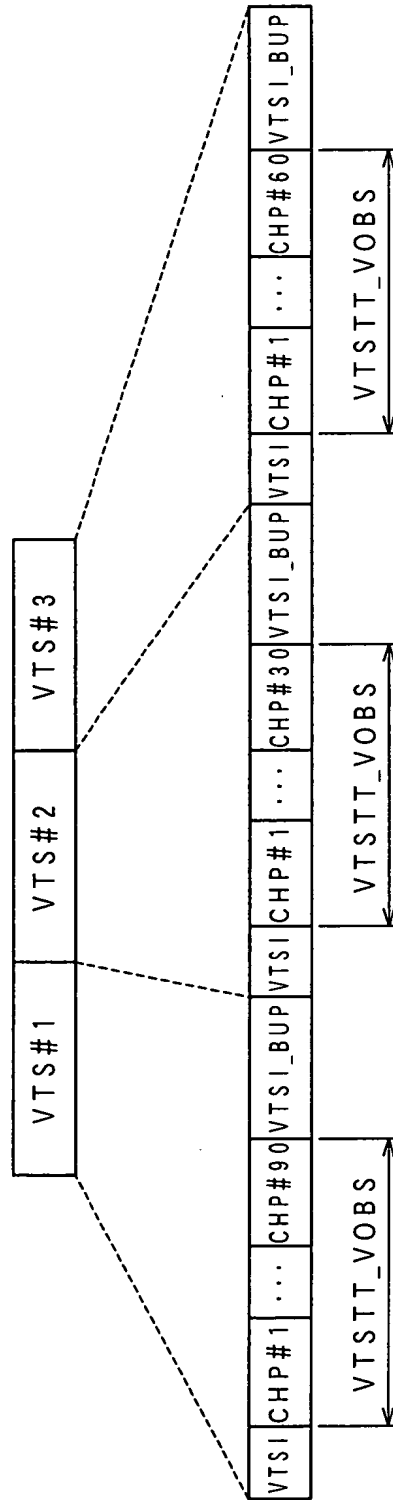


FIG.14

FIG.15A	Padding									
FIG.15B	Padding		VTSTT_V0BS		VTSL_BUP		Padding			
FIG.15C	Padd ing	TMP_VMG I	Padd ing	VTSL	VTSTT_V0BS	VTSL_BUP	Padding			
FIG.15D	Padd ing	TMP_VMG I	Padd ing	VTS #1		Padding	VTSTT_V0BS	VTSL_BUP	Padding	
FIG.15E	Padd ing	TMP_VMG I	Padd ing	VTS #1		VTSL	VTSTT_V0BS	VTSL_BUP	Padding	
FIG.15F	Padd ing	TMP_VMG I	Padd ing	VTS #1		VTS #2		Padding		
FIG.15G	LEAD-IN	UFD	TMP_VMG I	VMG	VTS #1	VTS #2		...	VTS #N	LEAD-OUT